

Amendments to the Claims

The following listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

1. – 10. (previously cancelled)

12. – 43. (previously cancelled)

44. (currently amended) A wireless communications device, comprising:

a serially-addressed memory for storing data, the serially-addressed memory accessible by a serial address and data line;

a processing unit directly connected to the serially-addressed memory by the serial address and data line, the processing unit having comprising: a serial memory interface controller for reading data from the serially-addressed memory upon a power on condition, the processing unit outputting at least a portion of the data read from the serially-addressed memory onto parallel address and data lines to a volatile memory; and

~~a read-only memory comprising code for providing instructions for reading data from a non-volatile memory;~~

~~the non-volatile memory for storing the data, the non-volatile memory connected to the serial memory interface controller by a serial address and data line;~~
~~a volatile-addressable memory for storing at least a portion of the data stored in the non-volatile memory, the volatile-addressable the volatile memory connected to the processing unit by the parallel address and data lines, the volatile memory for storing the at least a portion of the data read from the serially-addressed memory for later use by the processing unit;~~

a communications circuit connected to and controlled by the processing unit, the processing unit controlling the communications circuit utilizing the at least a portion of the data read from the volatile memory, the communications circuit comprising:

a transmitter circuit;

a receiver circuit; and

an antenna connected to the transmitter circuit and the receiver circuit.

45. (currently amended) The wireless communications device of claim 44, wherein the ~~non-volatile~~ serially-addressed memory is non-volatile serial memory.

46. (currently amended) The wireless communications device of claim 45, wherein the non-volatile serial memory is serial NAND flash memory.

47. (currently amended) The wireless communications device of claim 44, wherein the ~~non-volatile~~ serially-addressed memory is clocked parallel memory.

48. (currently amended) The wireless communications device of claim 44, wherein the ~~non-volatile~~ serially-addressed memory is indexed addressable memory.

49. (currently amended) The wireless communications device of claim 44, wherein the ~~non-volatile~~ serially-addressed is removably connected to the ~~serial memory interface controller processing unit by the serial address and data line.~~

50. (currently amended) The wireless communications device of claim 49, wherein the ~~non-volatile~~ serially-addressed is non-volatile serial memory comprising at least one of a multi-media card, a smart media card, a secure digital card and a memory stick.

51. (currently amended) The wireless communications device of claim 44, wherein the volatile addressable memory comprises at least one of a dynamic random access memory and a static random access memory.

52. (currently amended) The wireless communications device of claim 44, wherein the power on condition triggers the processing unit to determine ~~code of the read-only memory comprises: a first code section for determining whether the non-volatile~~ serially-addressed memory is connected to the processing unit, and to instruct ~~serial memory interface controller; and a second code section for instructing the serial memory interface controller~~ to transfer the at least a portion of the data from the ~~non-volatile~~ serially-addressed memory to the volatile addressable memory.

53. (currently amended) The wireless communications device of claim 44, wherein the at least a portion of the data stored in the ~~non-volatile~~ serially-addressed memory is critical operations data.

54. (previously added) The wireless communications device of claim 53, wherein the critical operations data is an application program that is critical to an operation of the wireless communications device.

55. (currently amended) The wireless communications device of claim 44, wherein the at least a portion of the data stored in the ~~non-volatile~~ serially-addressed memory is non-critical operations data comprising at least one of user interface information, a recent call list, a display setting, a roaming preference, a ringer preference, a non-critical application program, and a phone book.

56. (currently amended) A method for managing a wireless communications device, comprising the steps of:

executing instructions from a read-only memory in a processing unit, the instructions for directing a serial interface controller of the processing unit to read serial data from a non-volatile memory directly connected to the processing unit by a serial address and data line;

reading the serial data from the non-volatile memory over a the serial address and data line;

converting the serial data to parallel data;

transferring the parallel data to a volatile memory over parallel address and data lines;

reading at least a portion of the transferred data from the volatile memory; and operating a communications circuit of the wireless communications device in response to the at least a portion of the transferred data.

57. (previously added) The method of claim 56, wherein the non-volatile memory is a non-volatile serial memory.

58. (previously added) The method of claim 57, wherein the non-volatile serial memory is serial NAND flash memory.

59. (previously added) The method of claim 57, wherein the non-volatile memory is removable from the wireless communications device, further comprising the step of:
connecting the removable non-volatile memory to the wireless communications device.

60. (previously added) The method of claim 59, wherein the removable non-volatile memory is at least one of a multi-media card, a smart media card, a secure digital card and a memory stick.

61. (currently amended) A wireless communications device, comprising:
a wireless communications circuit comprising:
a receiver;
a transmitter; and
an antenna connected to the receiver and the transmitter;
a serial non-volatile memory;
a volatile memory; and
a processor connected to the wireless communications circuit, the processor comprising:
a serial interface controller directly connected to the serial non-volatile memory by a serial address and data line, and connected to the volatile memory by parallel address and data lines, the serial interface controller ~~reading~~ configured to read serial data from the serial non-volatile memory, ~~converting~~ to convert at least a portion of the serial data to parallel data, and ~~storing~~ to store the parallel data in the volatile memory; and

a read only memory for storing read instructions, the read instructions for instructing the serial interface controller to read the serial non-volatile memory upon a boot up condition of the wireless communications device;

wherein the processor controls the wireless communications circuit based upon the stored parallel data in the volatile memory.

62. (previously added) The wireless communications device of claim 61, wherein the serial non-volatile memory is removably connected to the serial interface controller.

63. (previously added) The wireless communications device of claim 61, wherein the serial non-volatile memory is NAND flash memory.